

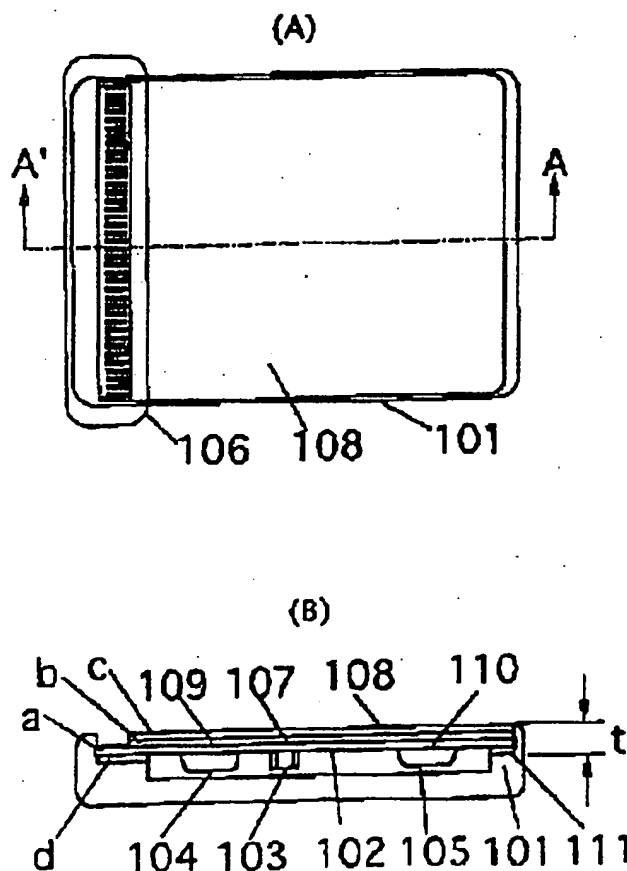
MEMORY CARD

Patent number: JP8190615
Publication date: 1996-07-23
Inventor: HARADA MASAOKI; ASAKURA TORU; YAMAGISHI TOSHIRO
Applicant: SEIKO EPSON CORP
Classification:
 - International: G06K19/077; B42D15/10; G11C5/00
 - european:
Application number: JP19950000993 19950109
Priority number(s):

Abstract of JP8190615

PURPOSE: To make the operation efficient, to improve the positional precision of seal sticking, and to improve appearance yield by sticking a seal on a substrate coated with a resist according to the border between the substrate and a frame or the opening part of 2nd resist.

CONSTITUTION: In the memory card, which has a package circuit board 110 with input and output terminals log for a card interface in the recessed part of the frame 101 adhered so that card surface is in level and has the nearly entire surface of the frame 101 covered with the package circuit board 110, the seal 108, where the contents, patterns, etc., of the memory card are printed, is stuck on the resist 109 of the package circuit board on the card appearance side. After the top surface of the package circuit board on the card appearance side is coated with 1st resist for wiring pattern protection and the 2nd resist which levels the surface so that the wiring pattern is invisible in the appearances, the opening part is formed in the 2nd resist to expose the 1st resist and the seal in the same shape with the opening part is stuck.



Data supplied from the esp@cenet database - Patent Abstracts of Japan